

<b>Esduino Pin Map</b>					
(Rev. 1 and higher)					
<b>MCU Pin #</b>	<b>MCU Pin Name</b>	<b>Arduino Mapping</b>	<b>Connector/Pin</b>	<b>Additional Connections</b>	<b>Comment</b>
1	PT0/IOC0/PW0	DIG3/PWM	J1-4	PE1/IRQ* via W2	cut to remove connection
2	PT1/IOC1/PW1	DIG5/PWM	J1-6		
3	PT2/IOC2/PW2	DIG6/PWM	J1-7		
4	PT3/IOC3/PW3	DIG9/PWM	J3-2		
5	VDD1	NONE			internal use
6	VSS1	NONE			internal use
7	PT4/IOC4/PW4	DIG10/PWM	J3-3	JB2-1,2	DIG10/PWM source is user-selectable via JB2
8	PT5/IOC5	DIG11/PWM	J3-4	JB3-1,4	DIG11/PWM source is user-selectable via JB3
9	PT6/IOC6	DIG7	J1-8		
10	PT7/IOC7	DIG8	J3-1		
11	MODC/BKGD	NONE			internal use
12	PB4	NONE	J5-1	JB6-2	for optional AUTO boot via DTR (unimplemented)
13	XCLKS*/PE7	NONE	J5-2		bonus pin
14	ECLK/PE4	NONE	J5-3		bonus pin
15	VSSR	NONE			internal use
16	VDDR	NONE			internal use
17	RESET*	RESET*	J4-1	pushbutton SW2	
18	VDDPLL	NONE			internal use
19	XFC	NONE			internal use
20	VSSPLL	NONE			internal use
21	EXTAL	NONE			internal use
22	XTAL	NONE			internal use
23	TEST/VPP	NONE			internal use
24	IRQ*/PE1	DIG3/PWM	J1-4	PT0/IOC0/PW0 via W2	cut to remove connection
25	XIRQ*/PE0	DIG2	J1-3	PE0/XIRQ* via W1	cut to remove connection
26	PA0	NONE	J5-4		bonus pin
27	AN0/PAD00	AN0	J2-1		
28	AN1/PAD01	AN1	J2-2		
29	AN2/PAD02	AN2	J2-3		
30	AN3/PAD03	AN3	J2-4		
31	AN4/PAD04	AN4	J2-5		
32	AN5/PAD05	AN5	J2-6		
33	AN6/PAD06	DIG2	J1-3	PE0/XIRQ* via W1	cut to remove connection
34	AN7/PAD07	DIG4	J1-5	J7-1	
35	VDDA	NONE			
36	VRH	AREF	J3-8	JB7-2,3	or VDD via JB7-1,2 (user-selectable)
37	VSSA	GND	J3-7, J4-4,5		
38	PS0/RXD	DIG0/RX	J1-1	JB8-1,3 and JB4	to USB or Xbee via JB4 (user-selectable)
39	PS1/TXD	DIG1/TX	J1-2	JB8-2,4 and JB4	to USB or Xbee via JB4 (user-selectable)
40	PM5/SCK	DIG13	J3-6	D2 and J6-5 and U4-6	
41	PM4/MOSI	DIG11/PWM	J3-4	JB3-1,2 and J6-2 and U4-5	DIG11/PWM pin assignment is user-selectable via JB3
42	PM3/SS*	DIG10/PWM	J3-3	JB2-2,3 and J6-4 and U4-1	DIG10/PWM pin assignment is user-selectable via JB2
43	PM2/MISO	DIG12	J3-5	J6-7 and U4-2	
44	PM1/TXCAN	DIG1/TX	J1-2	JB8-4,6 and J8-1	DIG1/TX pin assignment is user-selectable via JB8
45	PM0/RXCAN	DIG0/RX	J1-1	JB8-3,5 and J8-2	DIG0/RX pin assignment is user-selectable via JB8
46	VSSX	GND			
47	VDDX	NONE			
48	PP5/KWP5	DIG11/PWM	J3-4	JB3-1,3	DIG11/PWM pin assignment is user-selectable via JB3