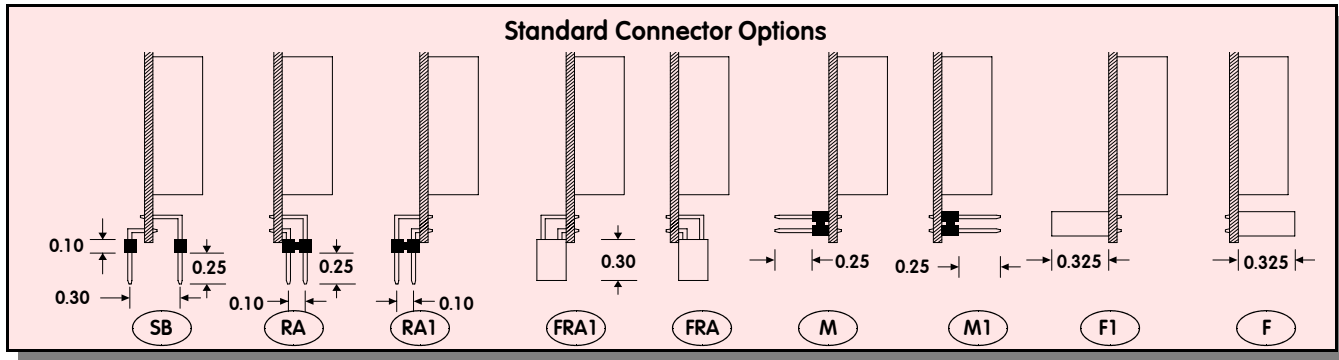


Adapt812DXLTM CONNECTOR PINOUTS

H1				H2			
PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME
1	PS4/MISO	50	GROUND	1	DATA15	50	VCC (+5VDC)
2	PS5/MOSI	49	GROUND	2	DATA14	49	GROUND
3	PS6/SCK	48	PS0/RXD0	3	DATA13	48	CSP1*
4	PS7/SS*	47	+5VDC	4	DATA12	47	CSP0*
5	PS1/TXD0	46	IRQ*	5	DATA11	46	CSD*
6	PT7/OC7/PAI	45	XIRQ*	6	DATA10	45	CS3*/PF3
7	PT6/OC6	44	RESET*	7	DATA9	44	CS2*/PF2
8	PT5/OC5	43	PE7/ARST	8	DATA8	43	CS1*/PF1
9	PT4/OC4	42	KWH0	9	DATA7/KWD7	42	CS0*/PF0
10	PT3/OC3	41	KWH1	10	DATA6/KWD6	41	ADDR21/PG5
11	PT2/OC2	40	KWH2	11	DATA5/KWD5	40	ADDR20/PG4
12	PT1/OC1	39	KWH3	12	DATA4/KWD4	39	ADDR19
13	PT0/OC0	38	KWH4	13	DATA3/KWD3	38	ADDR18
14	PJ7/KWJ7	37	KWH5	14	DATA2/KWD2	37	ADDR17
15	PJ6/KWJ6	36	KWH6	15	DATA1/KWD1	36	ADDR16
16	PJ5/KWJ5	35	KWH7	16	DATA0/KWD0	35	ADDR6
17	PJ4/KWJ4	34	PS2/RXD1	17	R/W*	34	ADDR7
18	PJ3/KWJ3	33	PE4/ECLK	18	ECLK/PE4	33	ADDR8
19	PJ2/KWJ2	32	PS3/TXD1	19	LSTRB*/PE3	32	ADDR9
20	PJ1/KWJ1	31	VRL	20	ADDR0	31	ADDR10
21	PJ0/KWJ0	30	VRH	21	ADDR1	30	ADDR11
22	PAD0/AN0	29	PAD4/AN4	22	ADDR2	29	ADDR12
23	PAD1/AN1	28	PAD5/AN5	23	ADDR3	28	ADDR13
24	PAD2/AN2	27	PAD6/AN6	24	ADDR4	27	ADDR14
25	PAD3/AN3	26	PAD7/AN7	25	ADDR5	26	ADDR15

NOTES: * indicates active low signal



Module:
#AD812DXLTM-□-□
Starter Package:
#AD812DXLTSP(-□-□)
(default -RA-M connectors unless otherwise specified)