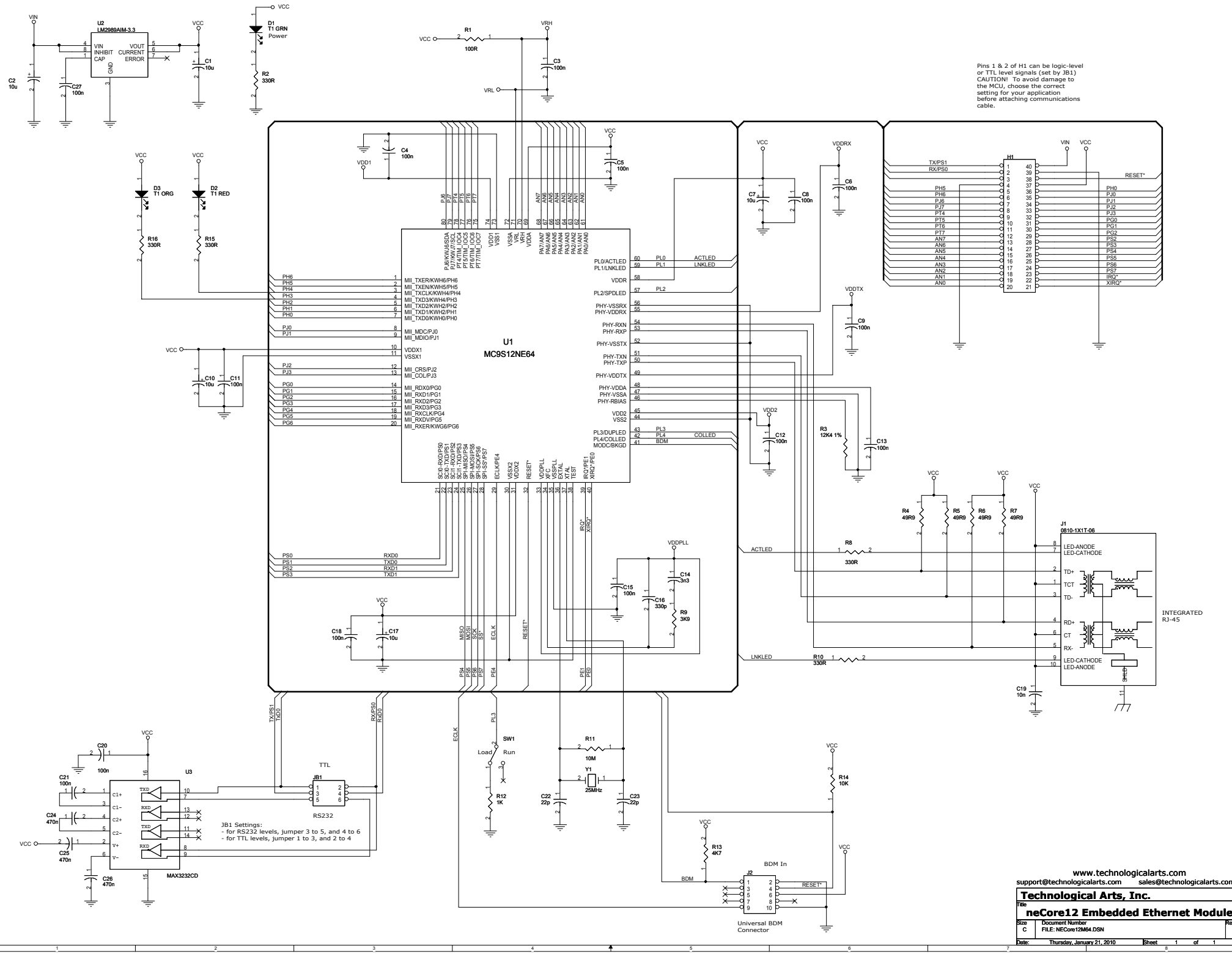
U2
LM2989AIM-3.3
VIN
INHIBIT
CAP
GND
VOUT
CURRENT
ERROR
C2 10u
C27 100n
C1 10u
D1 T1 GRN Power
R2 330R
R1 100R
C3 100n
C4 100n
C5 100n
D3 T1 ORG
D2 T1 RED
R16 330R
R15 330R
C10 10u
C11 100n
U1
MC9S12NE64
C7 10u
C8 100n
C6 100n
C9 100n
C12 100n
C13 100n
R3 12K4 1%
Pins 1 & 2 of H1 can be logic-level or TTL level signals (set by JB1) CAUTION! To avoid damage to the MCU, choose the correct setting for your application before attaching communications cable.
H1
R4 49R9
R5 49R9
R6 49R9
R7 49R9
R8 330R
R10 330R
J1
0810-1X1T-06
LED-ANODE
LED-CATHODE
TD+
TCT
TD-
RD+
CT
RX-
LED-CATHODE
LED-ANODE
SHLD
INTEGRATED RJ-45
C19 10n
C14 3n3
C15 100n
C16 330p
R9 3K9
C18 100n
C17 10u
C20 100n
C21 100n
C24 470n
C25 470n
C26 470n
U3
MAX3232CD
JB1
TTL
RS232
JB1 Settings:
- for RS232 levels, jumper 3 to 5, and 4 to 6
- for TTL levels, jumper 1 to 3, and 2 to 4
SW1
Load
Run
R12 1K
R11 10M
Y1 25MHz
C22 22p
C23 22p
R14 10K
R13 4K7
J2
BDM In
Universal BDM Connector
www.technologicalarts.com
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Technological Arts, Inc.
Title
neCore12 Embedded Ethernet Module
Size C
Document Number FILE: NECore12M64.DSN
Rev 1a
Date: Thursday, January 21, 2010
Sheet 1 of 1